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SERIAL NUMBER 10/621,055	FILING DATE 07/15/2003  RULE	CLASS 327	GROUP ART UNIT 2816	ATTORNEY DOCKET NO. IDTL002US1-1826
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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CIP of 10/094,101 03/08/2002 PAT 6,664,838  
 which claims benefit of 60/316,399 08/31/2001

yes

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/16/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials _____	STATE OR COUNTRY CA	SHEETS DRAWING 16	TOTAL CLAIMS 46	INDEPENDENT CLAIMS 4
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## TITLE

Clock processing logic and method for determining clock signal characteristics in reference voltage and  
 temperature varying environments

FILING FEE  RECEIVED 1302	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other
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